

Thyristor&Diode Module

 V_{RRM} / V_{DRM} 800 to 1600V

I_{FAV}/I_{TAV} 200 Amp I_{FRMS}/I_{TRMS} 314 Amp

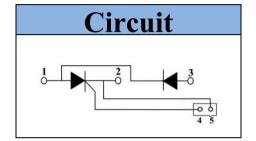
Features

- Aluminum oxide DBC
- Glass passivated chip
- High surge capability

Applications

- Power converters
- Lighting control
- DC motor control and drives
- Heat and temperature control





Module Type

Type	V_{RRM}/V_{DRM}	V_{RSM}
MFC200G-08	800V	900V
MFC200G-12	1200V	1300V
MFC200G-16	1600V	1700V

Maximum Ratings

Symbol	Item	Conditions	Values	Unit
I _{FAV} /I _{TAV}	Average On-state Current	180° Conduction Sin Half Wave, T _c = 82°C	200	А
I _{FRMS} /I _{TRMS}	RMS On-state Current		314	Α
I _{FSM} /I _{TSM}	Surge On-state Current	$T_j = 25^{\circ}C$, $t = 50Hz(10ms)$, $V_R = 0V$	6300	А
I ² t	Circuit Fusing Consideration	t = 10ms T _j =25°C	198450	A ² s
V _{ISO}	Isolation Breakdown Voltage	AC 50Hz/60Hz; R.M.S; 1min	3000	V
Tj	Operating Junction Temperature		-40 to + 125	°C
T _{stg}	Storage Temperature		-40 to + 125	°C
Mt	Mounting Torque	To Terminals(M6)	5±15%	
Ms	Mounting Torque	To Heatsink(M6)	5±15%	N⋅m
Weight	Module (Approximately)		180	g
di/dt	Critical Rate of Rise of On-state Current, Max	T_j = 125°C, V_D = 1/2 V_{DRM} , I_G = 150mA, di_G /dt = 0.1A/ μ s	150	A/μs

Thermal Characteristics

Symbol	Item	Conditions	Values	Unit
R _{th(j-c)}	Thermal Impedance, Max	Junction to Case(Per Leg)	0.13	°C/W
R _{th(c-s)}	Thermal Impedance, Max	Case to Heat Sink	0.05	°C/W

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■ Electrical Characteristics

Symbol	Item	Conditions	Values			Unit
			Min.	Тур.	Max.	Jint
$V_{\text{FM}}/V_{\text{TM}}$	Peak On-State Voltage, Max	$T_j = 25^{\circ}C$, $I_F/I_T = 600A$	-	-	1.60	V
I _{DRM} /I _{RRM}	Repetitive Peak Reverse Current, Max /Repetitive Peak Off-state Current, Max	$T_j = 125$ °C, $V_R = V_{RRM}$, $V_D = V_{DRM}$	-	-	35	mA
V_{GT}	Gate Trigger Voltage, Max	$T_j = 25^{\circ}C, V_D = 6V$	-	-	3.0	V
I _{GT}	Gate Trigger Current, Max	$T_j = 25^{\circ}C, V_D = 6V$	-	-	150	mA
V_{GD}	Gate Non-Trigger Voltage, Max	$T_j = 125$ °C, $V_D = 2/3V_{DRM}$	-	-	0.25	V
IL	Latching Current	T _j = 25°C	-	200	-	mA
I _H	Holding Current	T _j = 25°C	-	150	-	mA
t _{gt}	Turn On Time	T _j = 25°C	-	3	-	μs
dv/dt	Critical Rate of Rise of Off-state Voltage, Min	T _j = 125°C, V _D = 2/3V _{DRM} Linear Voltage Rise	1000		V/µs	
V _{T0}	Threshold Voltage, for power loss calculation only	T _j = 125°C	0.86			V
ľΤ	Slope Resistance, for power loss calculation only	T _j = 125°C	1.3		mΩ	

Performance Curves

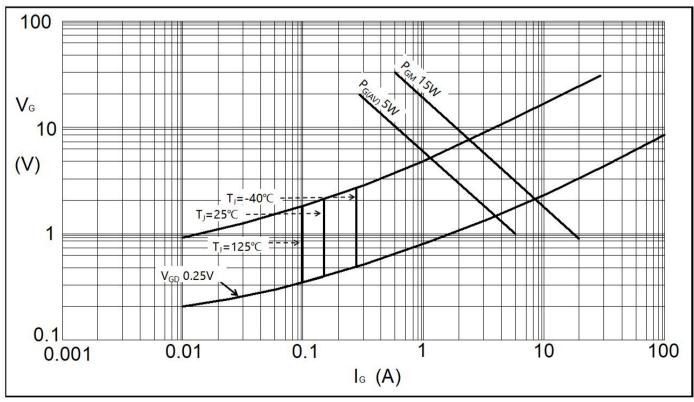


Fig1. Gate Trigger Characteristics



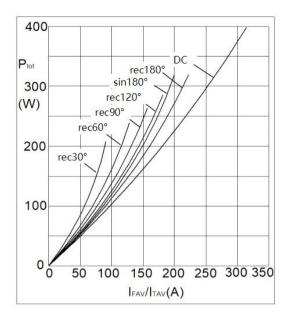


Fig2. Power Dissipation

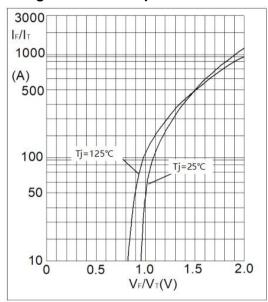


Fig4. Forward Characteristics

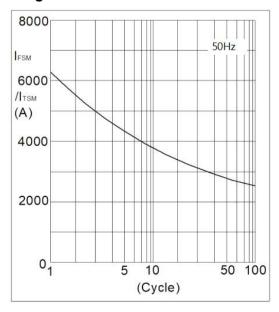


Fig6. Max Non-Repetitive Forward Surge Current

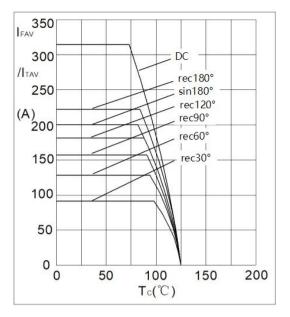


Fig3. Forward Current Derating Curve

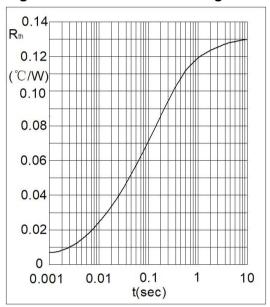
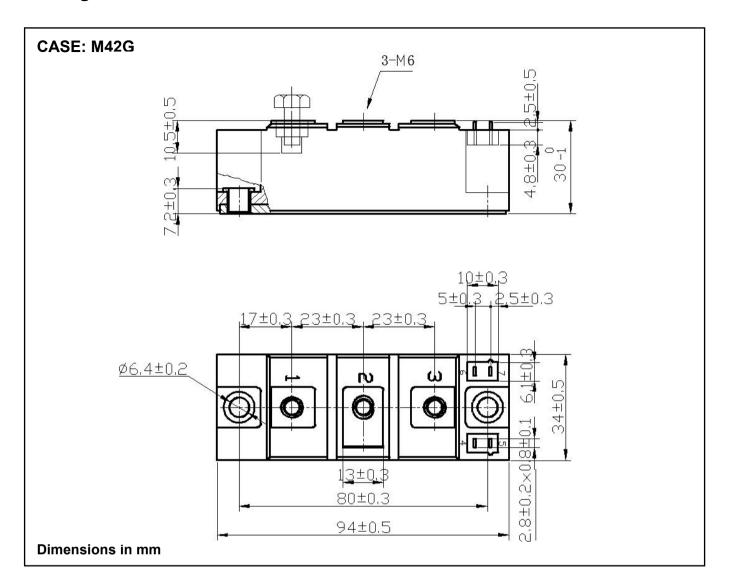


Fig5. Transient Thermal impedance



Package Outline Information



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